


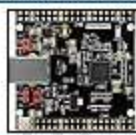



## FT600/FT601 FPGA USB Board

No	Description	FPGA	Series	Image	Features	DOC
5	USB 3.0 with Cyclone 10 LP USB-FPGA board	10CL080YF780C8G 10CL120YF780C8G	EDA-013		<b>Cyclone 10 LP</b> <b>SuperSpeed</b> <b>USB Config.</b> <b>USB Comm.</b> <b>SDRAM</b> <b>SPI-Flash</b>	Document
4	CycloneV USB-FPGA board	5CEBA4F23C8N	EDA-009		<b>Cyclone V</b> <b>SuperSpeed</b> <b>USB Config.</b> <b>USB Comm.</b>	Document
3	Kintex-7 USB-FPGA board	XC7K70T-1FBG484C XC7K160T-1FBG484C	EDX-009		<b>Kintex-7</b> <b>SuperSpeed</b> <b>USB Config.</b> <b>USB Comm.</b> <b>DDR3</b>	Document
2	FT601 Evaluation Board		USB-107		<b>SuperSpeed</b>	Document
1	FT600 Evaluation Board		USB-106		<b>SuperSpeed</b>	Document

### Key Device Features:

- Support for USB3.0 SuperSpeed (5Gbps), USB High Speed (480Mbps) and USB 2.0 Full Speed (12Mbps) transfer
- Available with 16bit wide parallel FIFO interface
- Supports 2 parallel slave FIFO bus protocols, with data bursting rate up to 200Mbps
- Supports multi channel FIFO interface
- Up to 8 configurable endpoints (pipes)
- Built-in 16kB FIFO data buffer RAM
- Supports multi voltage I/O: 1.8V, 2.5V and 3.3V (USB-106 supports only 3.3V)
- Configurable GPIO support
- Supports Remote Wakeup capability
- Programmable ROM
- Integrated 1V regulator
- Provides USB Battery Charger Detection (BC1.2.)
- Integrated power-on-reset circuit
- Extended operating temperature range: -40° C to 85° C